

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Li

Serial No. 09/670,874



Group Art Unit: 2826

Filed: September 27, 2000

Examiner: Victor Mandela

For: Semiconductor Integrated Circuit Device

9/18/03  
P Walker  
1-22-03

**INFORMATION DISCLOSURE STATEMENT**

And the fee set forth in 1.17(p)

Commissioner for Patents  
Washington, D.C. 20231

Date: January 13, 2003

Sir:

In consideration of Applicant's duty of disclosure under 37 C.F.R. §1.56 and 37 C.F.R. §1.97, Applicant wishes to bring to the Examiner's attention the documents listed on the attached Form PTO-1449. A copy of each reference is provided for the Examiner's convenience.

The fee of one hundred and eighty dollars (\$180) for acceptance of the Information Disclosure should be charged to the undersigned deposit account 15-1770.

In the event any additional fees are required with respect to this paper, please charge the Deposit Account No. 50-1770 and overpayment should be credited to this account.

Respectfully submitted,

  
James A. Poulos, III  
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Registration No. 31,714

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Enclosures: PTO-1449  
8 references enclosed